

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,862,645 B2  
APPLICATION NO. : 09/766246  
DATED : March 1, 2005  
INVENTOR(S) : Paul Garnett

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 9,

Lines 42-44, please delete "register read/writeable and is configured to store data representative of a processor identification," and substitute

-- register comprising:

a first field which is read/writeable and which is configured to store data representative of the processor version of the processor; and

a second field which is configured to store data representative of a configuration of the processor, --;

Line 47, please delete "sets into its processor" and substitute -- sets into said first field of its processor --;

Column 10,

Line 10, please delete "that includes a plurality" and substitute -- that comprises a plurality --;

Lines 14-16, please delete "is read/writeable and is configured to store data representative of a processor identification," and substitute

-- register comprises:

a first field which is read/writeable and which is configured to store data representative of a processor version of the processor; and

a second field which is configured to store data representative of a configuration of the processor, --;

Line 18, please delete "into said processor" and substitute -- into said first field of said processor --;

Lines 21-22, please delete "of a processor identification." and substitute -- of a processor version. --;

Lines 29-30, please delete "each of which processing" and substitute -- each of said processing --;

Line 31, please delete "processor, at least" and substitute -- processor, wherein at least --;

Line 33, please delete "register said method comprising the steps of" and substitute

-- register comprising:

a first field which is read/writeable and which is configured to store data representative of a processor version of the processor; and

a second field which is configured to store data representative of a configuration of the processor, said method comprising: --;

Lines 38-39, please delete "into said processor" and substitute -- into said first field of process --;

Lines 41-42, please delete "said processor identification." and substitute -- said processor version. --;

Lines 53, please delete "a processing sets" and substitute -- a processing set --.

Lines 62-63, please delete "method comprising the steps of:" and substitute -- method comprising: --;

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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 11,

Lines 2-4, please delete "register which is read/writeable and has data representative of a processor" and substitute

-- register comprising:

a first field which is read/writeable and which has data representative of a processor version of the processor stored therein; and

a second field which has data representative of a configuration of the processor stored therein, --;

Line 7, please delete "via said I/O bus into said processor" and substitute -- via said I/O bus into said first field of said processor --;

Column 12,

Lines 2-3, please delete "said processor identification." and substitute -- said processor version. --.

Signed and Sealed this

Twenty-fifth Day of July, 2006

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a distinct "D" at the end.

JON W. DUDAS  
*Director of the United States Patent and Trademark Office*